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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application of: Fukutomi *et al.*

Confirmation No.:

Serial No.: 10/705,706

Art Unit: 2813

Filed: November 10, 2003

Examiner: To be assigned

For: FABRICATION PROCESS OF
SEMICONDUCTOR PACKAGE AND
SEMICONDUCTOR PACKAGE

Attorney Docket No.: 7426-083-999

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
PO Box 1450
Alexandria, VA 22313-1450 Sir:

In accordance with the duty of disclosure provisions of 37 C.F.R. §1.56, there is hereby provided certain information which the Examiner may consider material to the examination of the subject U.S. patent application. It is requested that the Examiner make this information of record if it is deemed material to the examination of the application.

Enclosures accompanying this Information Disclosure Statement are a list of all foreign patents, publications, applications, or other information submitted for consideration by the office and a legible copy of each publication or that portion which caused it to be listed on the PTO-1449;

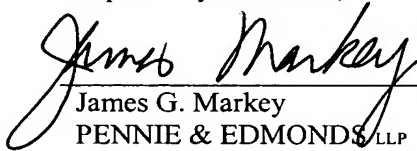
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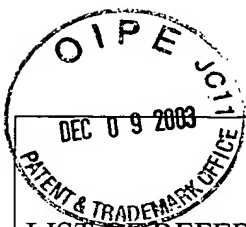
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Respectfully submitted,

Date: December 9, 2003


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LIST OF REFERENCES CITED BY APPLICANT

(Use several sheets if necessary)

ATTY DOCKET NO.

7426-083-999

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GROUP

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U.S. PATENT DOCUMENTS

*EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
A33	4,969,257	11/13/90	Sato <i>et al.</i>			

FOREIGN PATENT DOCUMENTS

DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION
					YES NO

OTHER REFERENCES (Including Author, Title, Date, Pertinent Pages, Etc.)

EXAMINER

DATE CONSIDERED

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.